IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:	Jean-Yves SIMON, et al.	§	Confirmation No.:	9476
Serial No.:	10/764,670	§ §	Group Art Unit:	2112
Filed:	January 26, 2004	8 8	Examiner:	Fritz Alphonse
For:	Method for Streamlining Error Correction Code Computation While Reading or Programming a NAND Flash Memory	8 8 8 8 8	Docket No.:	TI-36989 (1962-09800)

RESPONSE TO OFFICE ACTION DATED FEBRUARY 22, 2008

Mail Stop Amendment Date: May 20, 2008
Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

INTRODUCTORY COMMENTS

Sir:

In response to the Office Action dated February 22, 2008, the Examiner is requested to enter the following amendments and consider the accompanying remarks. Reconsideration is respectfully requested.

Amendments to the Claims are reflected in the listings of claims which begin on page 2 of this paper.

Remarks begin on page 6 of this paper.

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